

**GATE DRIVER WITH A DC OFFSET BIAS CIRCUIT AND
A POWER CONVERTER EMPLOYING THE SAME**

ABSTRACT OF THE DISCLOSURE

A gate driver and a method of driving a switch for use with a power converter having a main active clamp circuit associated with a main power switch coupled to a primary winding of a transformer and a rectifier switch coupled to a secondary winding of the transformer. The main power switch conducts during a main conduction period of the power converter and the rectifier switch conducts during an auxiliary conduction period of the power converter. In one embodiment, the gate driver includes a DC offset bias circuit, coupled to a secondary winding of the transformer, that provides a gate drive signal having a DC bias voltage to a gate terminal of the rectifier switch.